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Design the architecture of a 4-bit capacitor DAC. 12.15 A bias generator is required to generate 16 steps from 0 to 100 μ A to bias an amplifier. Design a CMOS DAC to do this, assuming the presence of a 50 μ A reference current. 12.16 Differential circuits provide good noise immunity and have the advantage of dual rail inputs and outputs.

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A revised guide to the theory and implementation of CMOS analog and digital IC design The fourth edition of CMOS: Circuit Design, Layout, and Simulation is an updated guide to the practical design of both analog and digital integrated circuits.

CMOS: Circuit Design, Layout, and Simulation, 4th Edition ...
Textbook: Principles of CMOS VLSI Design: A Circuit and Systems Perspective (4th Edition), By Neil Weste, David Harris, Published by Addison-Wesley, c2010, ISBN 978-0321547743. Note that there are some Errata (mistakes) that are listed here.

CS/EE 5710/6710 Digital VLSI

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